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MAR 12 2009S/N 10/829,559PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Asher Hazanchuk
Serial No.: 10/829,559
Filed: April 22, 2004
Title: Method and Apparatus for Implementing a Multiplier Utilizing Digital Signal Processor Block Memory Extension

Examiner: Do, Chat C
Group Art Unit: 2193
Docket No.: ALT.P030 (A01252)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir,

In response to the Office Action mailed November 13, 2008, Applicants respectfully request the Examiner to consider the following remarks. A petition for a one month extension of time is being submitted herewith.

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